

INTERNATIONAL STANDARD

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**Surface mounting technology –
Part 1: Standard method for the specification of surface mounting components
(SMDs)**

**Technique du montage en surface –
Partie 1: Méthode normalisée pour la spécification des composants montés en
surface (CMS)**



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INTERNATIONAL ELECTROTECHNICAL COMMISSION

SURFACE MOUNTING TECHNOLOGY –**Part 1: Standard method for the specification
of surface mounting components (SMDs)**

FOREWORD

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International Standard IEC 61760-1 has been prepared by IEC technical committee 91: Surface mounting technology.

This third edition cancels and replaces the second edition published in 2006. This edition constitutes a technical revision.

This edition includes the following significant technical changes with respect to the previous edition:

- a) inclusion of additional mounting methods: conductive glue bonding, sintering and solderless interconnection.

The text of this International Standard is based on the following documents:

FDIS	Report on voting
91/1648/FDIS	91/1653/RVD

Full information on the voting for the approval of this International Standard can be found in the report on voting indicated in the above table.

This document has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts in the IEC 61760 series, published under the general title *Surface mounting technology*, can be found on the IEC website.

The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under "<http://webstore.iec.ch>" in the data related to the specific document. At this date, the document will be

- reconfirmed,
- withdrawn,
- replaced by a revised edition, or
- amended.

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INTRODUCTION

Specifications for electronic components have in the past been formulated for each component family. The regulations for environmental tests have been selected from IEC 60068 and other IEC and ISO publications. The intention for this procedure was that all components, once installed in a piece of equipment, had to satisfy certain criteria.

The introduction and increasing use of different mounting processes on one assembly make it necessary to extend the existing requirements to include those arising from processing during assembly.

Nevertheless, there existed no harmonized standard that prescribes the content of a component specification before the publication of IEC 61760-1. It is the purpose of this document to define the general requirements for component specifications derived from the assembly processes. This is done in three steps.

In the first step, general requirements for component specifications and component design related to the handling and placement of the component on the substrate are given (Clause 4). In the second step, the requirements related to assembly processes are given (Clause 5). In the third step, additional requirements resulting from specific mounting methods are given (Clauses 6 to 9).

Mixed technology boards, i.e. boards containing through-hole components and SMDs, require additional consideration with respect to the through-hole components. These may be subject to the same requirements as the SMDs. Persons responsible for drafting specifications for "non-surface mounting components" wishing to include a statement on their ability to withstand surface mounting conditions should use the classifications and tests set out in the present document.

SURFACE MOUNTING TECHNOLOGY –

Part 1: Standard method for the specification of surface mounting components (SMDs)

1 Scope

This part of IEC 61760 defines requirements for component specifications of electronic components that are intended for usage in surface mounting technology. To this end, it specifies a reference set of process conditions and related test conditions to be considered when compiling component specifications.

The objective of this document is to ensure that a wide variety of SMDs can be subjected to the same placement, mounting and subsequent processes (e.g. cleaning, inspection) during assembly. This document defines tests and requirements that need to be part of any SMD component's general, sectional or detail specification. In addition, this document provides component users and manufacturers with a reference set of typical process conditions used in surface mounting technology.

Some of the requirements for component specifications in this document are also applicable to components with leads intended for mounting on a circuit board. Cases for which this is appropriate are indicated in the relevant subclauses.

2 Normative references

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60068 (all parts), *Environmental testing*

IEC 60068-2-2, *Environmental testing – Part 2-2: Tests – Test B: Dry heat*

IEC 60068-2-21, *Environmental testing – Part 2-21: Tests – Test U: Robustness of terminations and integral mounting devices*

IEC 60068-2-45:1980, *Basic environmental testing procedures – Part 2-45: Tests – Test XA and guidance: Immersion in cleaning solvents*
IEC 60068-2-45:1980/AMD1:1993

IEC 60068-2-58, *Environmental testing – Part 2-58: Tests – Test Td: Test methods for solderability, resistance to dissolution of metallization and to soldering heat of surface mounting devices (SMDs)*

IEC 60191-6, *Mechanical standardization of semiconductor devices – Part 6: General rules for the preparation of outline drawings of surface mounted semiconductor device packages*

IEC 60194-2, *Printed boards design, manufacture and assembly – Vocabulary – Part 2: Common usage in electronic technologies as well as printed board and electronic assembly technologies*